

In the Claims

This listing of claims will replace all prior versions and listings of claims in the application:

Claim 1. (Canceled)

1        2. (Original) A computer implemented method of rasterizing a  
2 page in a page description language in a multiprocessor integrated  
3 circuit comprising the steps of:  
4        interpreting said page in said page description language with  
5 a first processor of said multiprocessor integrated circuit;  
6        spawning a subtask from said first processor to another of  
7 said processors for sorting polygon edges in increasing minimum Y  
8 coordinate.

1        3. (Original) The computer implemented method of claim 4,  
2 wherein:  
3        said first processor is a reduced instruction set processor  
4 having a floating point computation unit; and  
5        each of said other processors is a digital signal processor  
6 having an integer multiplier unit.

1        4. (Original) The computer implemented method of claim 5,  
2 further comprising:  
3        spawning a subtask from said first processor to another of  
4 said processors for detecting a Y coordinate of edge intersection  
5 via successive midpoint approximation.

1        5. (Original) The computer implemented method of claim 5,  
2 further comprising:  
3        calculating a Y coordinate of edge intersection employing said  
4 floating point calculation unit of said first processor.

Claims 6 to 10. (Canceled)